

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
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YASUDA et al.)	
)	
Application Number: To be Assigned)	
)	Art Unit 2181
Filed: Concurrently herewith)	
)	
For: SHARED MEMORY MULTIPROCESSOR)	
PERFORMING CACHE COHERENCE)	
CONTROL AND NODE CONTROLLER)	
)	
Attorney Docket No. ASAM.0003)	

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to an examination on the merits, please amend the above identified application as follows: